Dr. Priyanka Malik

Journals

Malik, P, Gupta, R.S and Gupta, M (2013) Linearity Analysis of GME-TRC MOSFET Using SiO_2 as a Gate Insulator at High Temperature. *Advanced Materials Research* 685:207-210.

Malik, P, Gupta, R.S, Chaujar, R et al. (2012) AC Analysis of Nanoscale GME-TRC MOSFET for Microwave and RF Applications. *Microelectronic Reliability* 52(1):151-158.

Malik, P, Gupta, R.S, Chaujar, R et al. (2011) Linearity-Distortion analysis of GME-TRC MOSFET for High Performance and Wireless Applications. *Journal of Semiconductor Technology and Science* 11(3):162-174.

Malik, P, Chaujar, R, Gupta, M et al. (2010) Physics Based Threshold Voltage Analysis of Gate Material Engineered Trapezoidal Recessed Channel (GME-TRC) Nanoscale MOSFET and its Multilayered Gate Architecture. *International Journal of Microwave and Optical Technology* 5(6):361-368.

Malik, P, Chaujar, R, Gupta, M et al. (2010) Two-Dimensional Analytical Drain Current Model for Multilayered-Gate Material Engineered Trapezoidal Recessed Channel (MLGME-TRC) MOSFET: A Novel Design. *World Academy of Science, Engineering and Technology* 64(0):472-476.

Malik, P, Kumar, S.P, Chaujar, R et al. (2010) Gate Material Engineered-Trapezoidal Recessed Channel MOSFET for High- Performance Analog and RF. *Applications and Optical Technology Letters* 52(3):694-698.